

November 13–14, 2024 | Online

# Neural network-based sensorless FOC of PMSM using AURIX<sup>™</sup> TC4x PPU

Dr. Marko Gecic, Infineon Technologies AG





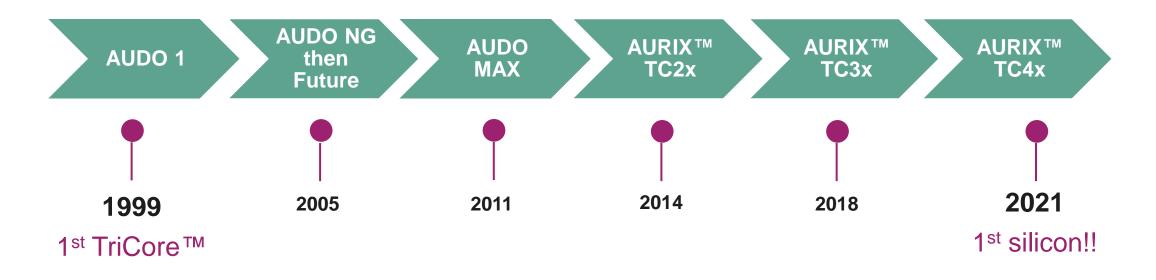
### TriCore<sup>™</sup> is the trusted choice for Automotive, with shipments exceeded 1 Billion units in 2022





#### The TriCore<sup>™</sup> concept was born in 1999

TriCore<sup>™</sup> integrates three functions: DSP, RISC & MCU The success story started in Powertrain and spread to the entire automotive MCU market





More than 1,000,000,000 TriCore<sup>™</sup> shipped since 1999 with outstanding quality <<1ppm



### The AURIX<sup>™</sup> TC4x meets these future needs and more, providing the industries most extensive major upgrade path for auto MCUs

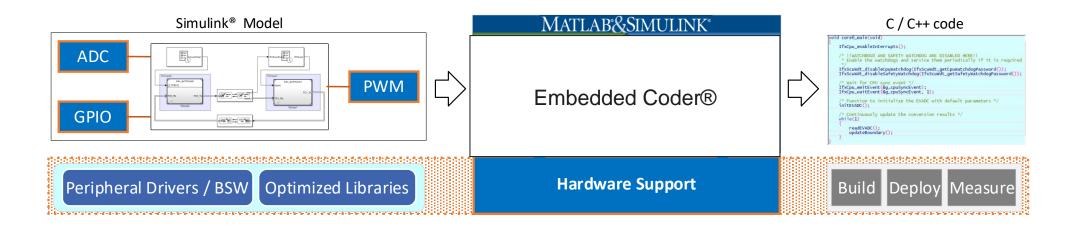


		Headroom to grow	<ul> <li>Feature rich to offer applications headroom to grow</li> <li>Scalable family HW and SW concept for platform reuse</li> </ul>
	•	High Performance with Al	<ul> <li>More processing power from TriCore<sup>™</sup>v1.8 with virtualization support and new AURIX<sup>™</sup> Accelerator Suite</li> <li>Parallel Processing Unit (PPU) for affordable AI</li> </ul>
	•	New E/E architecture	<ul> <li>Optimized devices for Zone and Domain control,</li> <li>Optimized devices for complex sensor and actuator control</li> </ul>
		Fully connected and secured	<ul> <li>Enhanced connectivity, new high-speed interfaces</li> <li>Data Routing Engine for efficient communication</li> <li>Cutting edge security features to protect with future post quantum cryptography → ISO/SAE 21434 certified</li> </ul>
Whilst ensuring dependability		Fast Time to Market	<ul> <li>Seamless "Ease of Use" tool chain and software offering</li> <li>Model based design for reduced R&amp;D effort</li> <li>Early development support based on virtual prototyping</li> </ul>



#### **AURIX™ TC4x Hardware Support Package (HSP)**





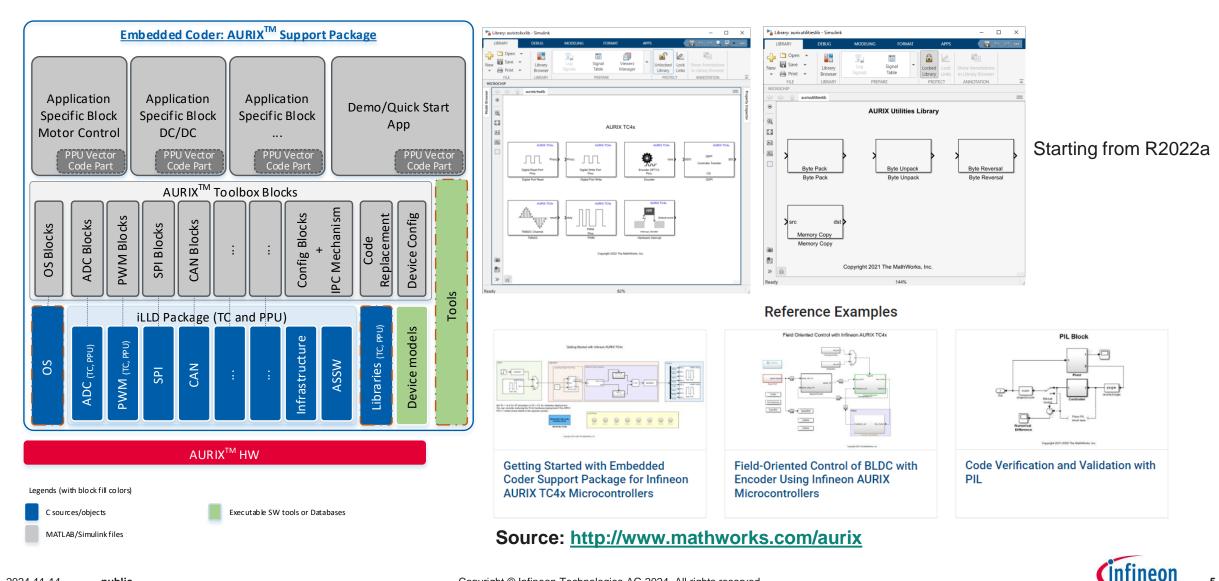
#### Hardware Support Package (HSP)

- Is a plugin to MathWorks Embedded Coder®
- Provides simulation support with target (PIL)
- Aids to generate target specific optimized code
- Connects application to the peripherals  $\rightarrow$  calls to driver APIs
- Provides HW mapping → Configuration Graphical User Interface (GUI)



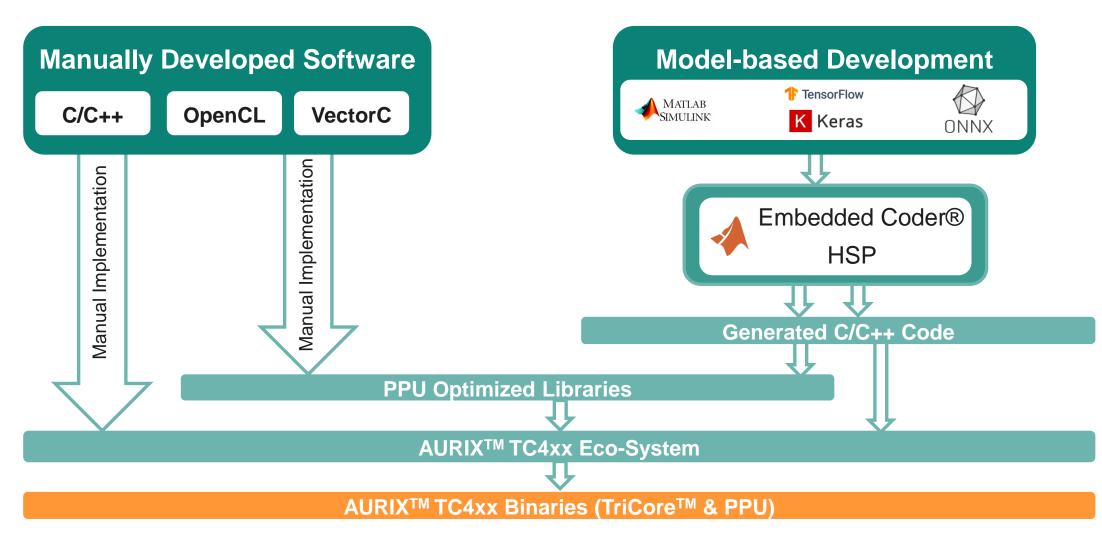


#### **AURIX™ TC4x Hardware Support Package (HSP)**





#### **Embedded Software Development Landscape for AURIX™ TC4x**

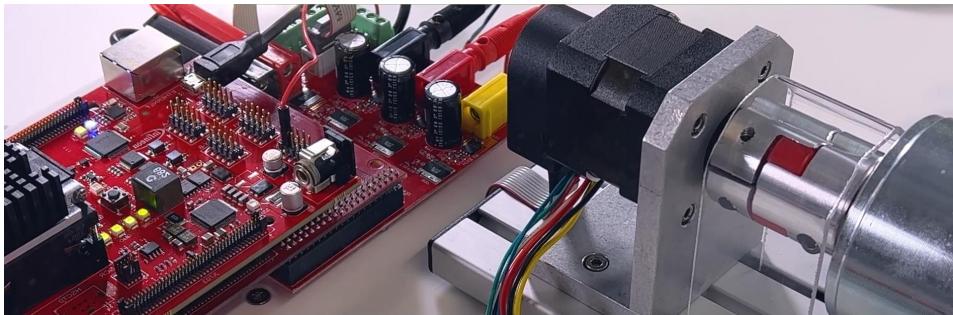


Infineon

### NN-based sensorless FOC of PMSM using AURIX<sup>™</sup> TC4x PPU<sup>™</sup> Description of the demonstrator



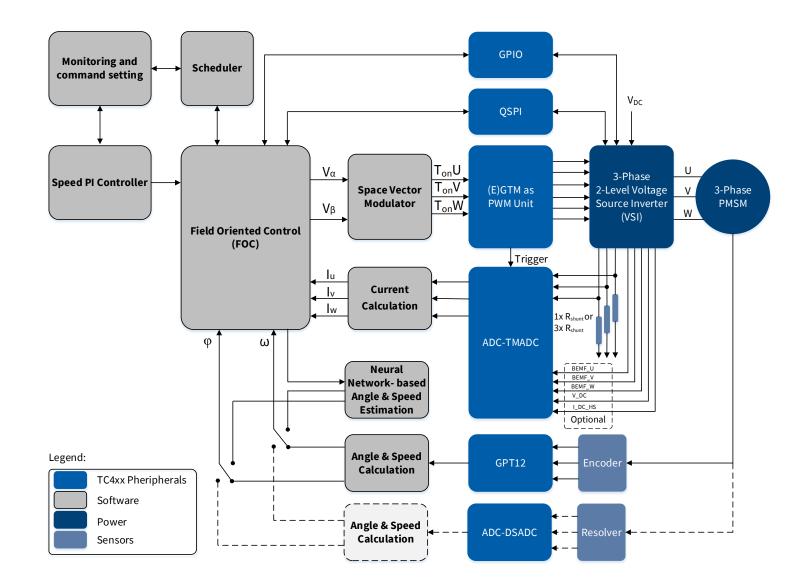
- Artificial Intelligence (AI) offers new use cases for advanced data processing:
  - to achieve an improved system performance, e.g. in terms of energy-efficiency
  - to achieve potential cost advantages through replacement of hardware by intelligent software
- In the domain of electrification, energy-efficiency and cost-efficiency are of highest interest
- This demonstrator shows an implementation of neural network-based sensorless Field Oriented Control (FOC) of a Permanent Magnet Synchronous Motor (PMSM)





### NN-based sensorless FOC of PMSM using AURIX<sup>™</sup> TC4x PPU<sup>™</sup> Simplified system block diagram

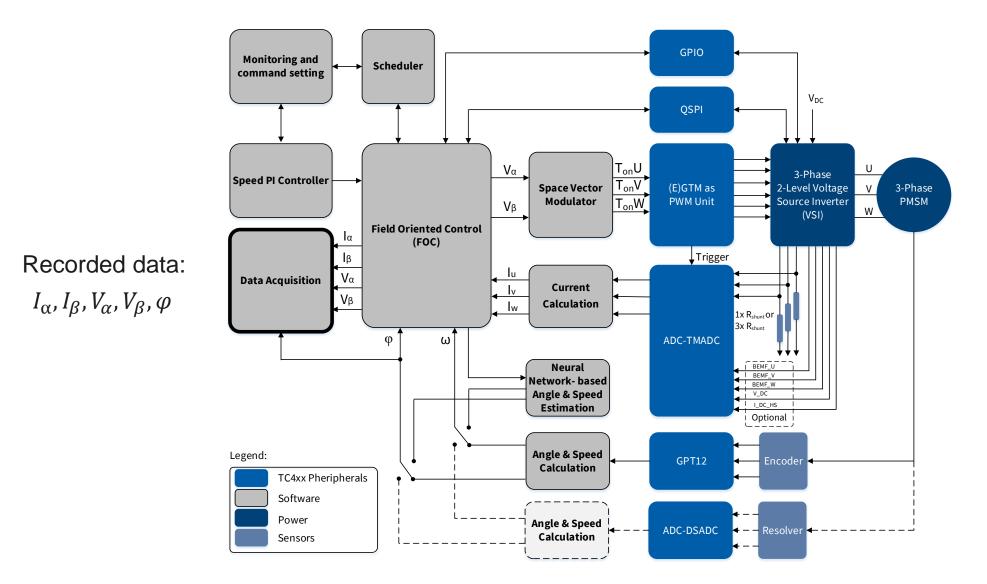






### NN-based sensorless FOC of PMSM using AURIX<sup>™</sup> TC4x PPU<sup>™</sup> Development process: Data acquisition

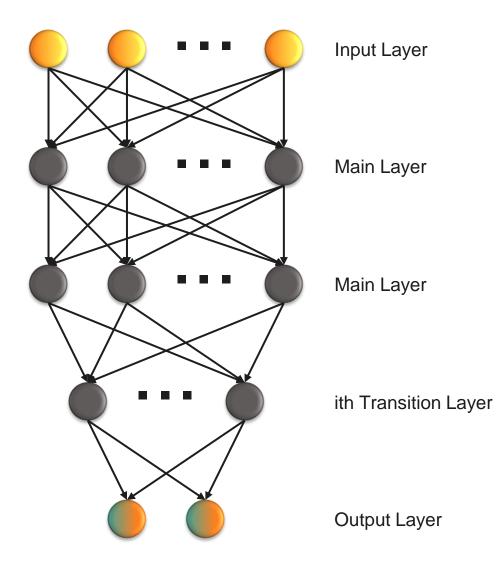






## NN-based sensorless FOC of PMSM using AURIX<sup>™</sup> TC4x PPU<sup>™</sup> Development process: Training of multilayer perceptron (MLP)





#### **Model Selection**

- Different input sizes
- Different layer sizes
- Different number of layers
- Different optmizers
- Different model types

• • • •

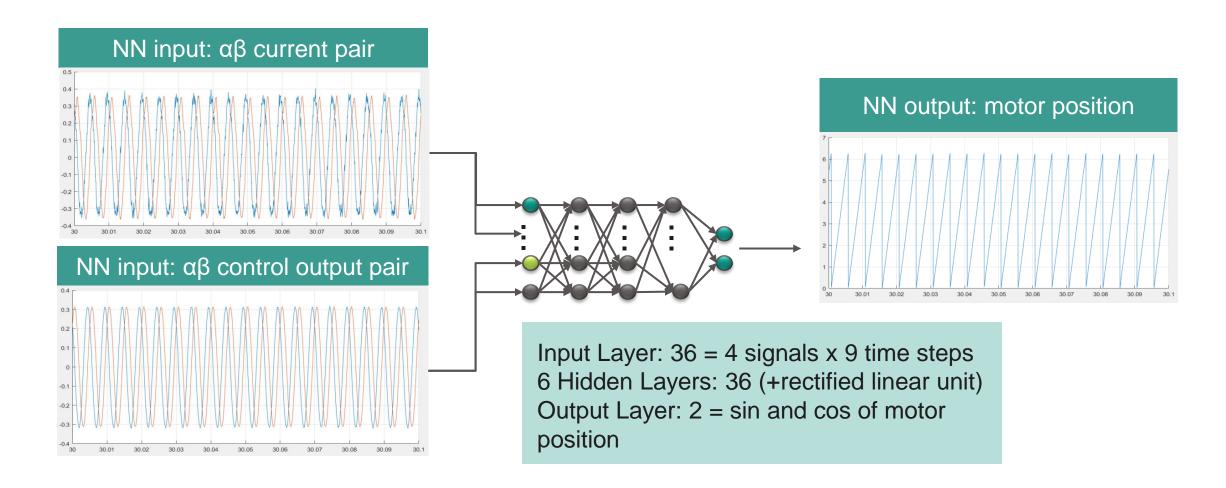
#### **MLP Scaling**

- Input layer size:  $S_{in} = N * T_{in} = 4 * T_{in}$ 
  - # Main layers:  $2 + \max(0, ceil(T_{in}) 2)$
  - Size Main layers: *S*<sub>in</sub>
  - # Trans. layers:  $l_t = max(0, floor(log(S_{in}, S_{out})) 3)$
  - Size ith Trans. layers:  $2^{(l_t + 2 i)}$
  - Output layer size:  $S_{out} = M * T_{out} = 2 * 1 = 2$



### NN-based sensorless FOC of PMSM using AURIX<sup>™</sup> TC4x PPU<sup>™</sup> Development process: Evaluation of multilayer perceptron (MLP)

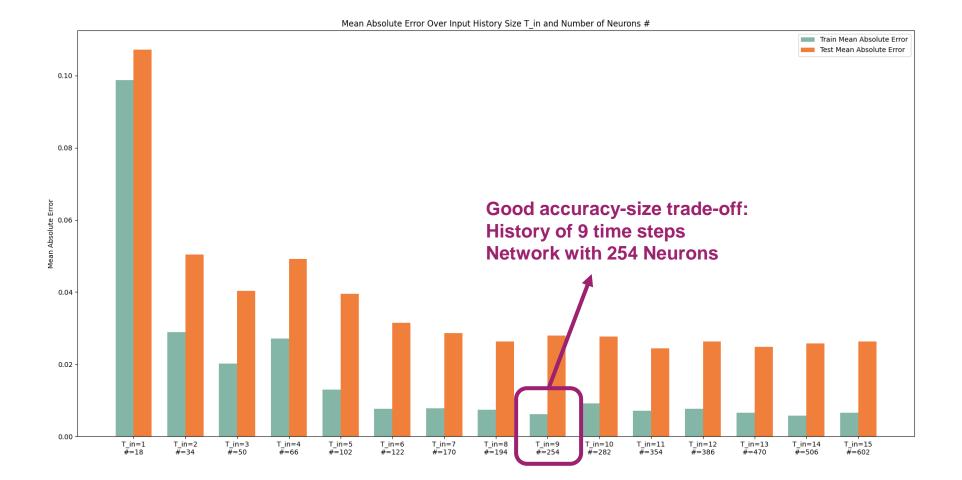






### NN-based sensorless FOC of PMSM using AURIX<sup>™</sup> TC4x PPU<sup>™</sup> Development process: Evaluation of multilayer perceptron (MLP)





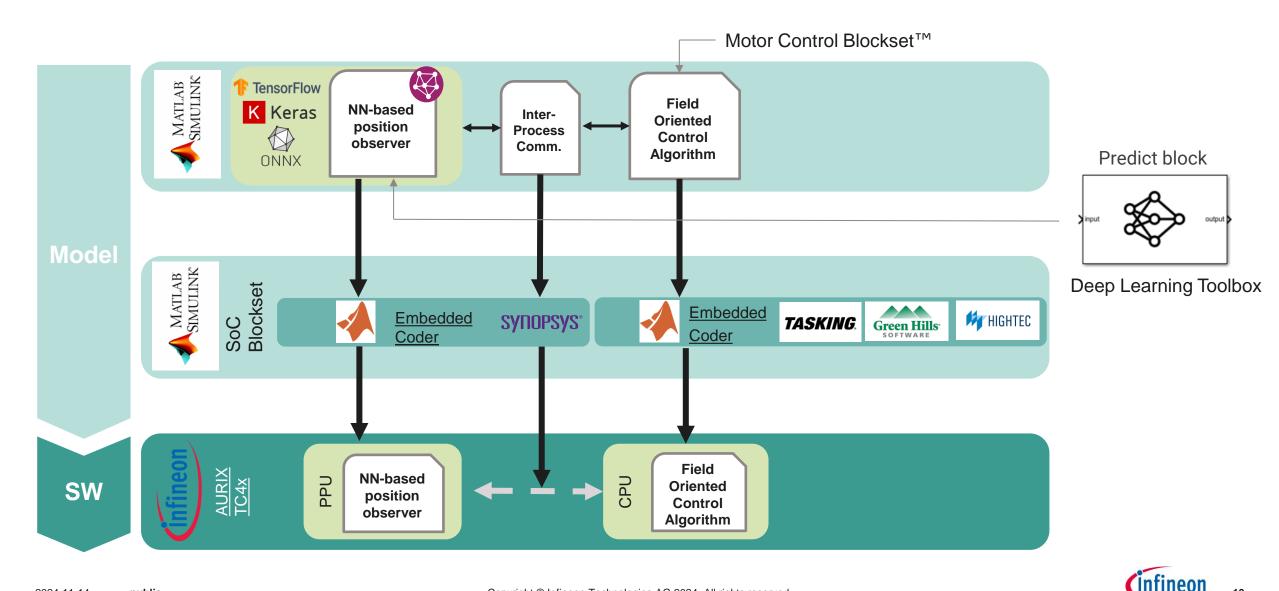


2024-11-14 **public** 

12

#### Partitioning of the motor control application using MathWorks **Embedded Coder**<sup>®</sup> support for AURIX<sup>™</sup> TC4x

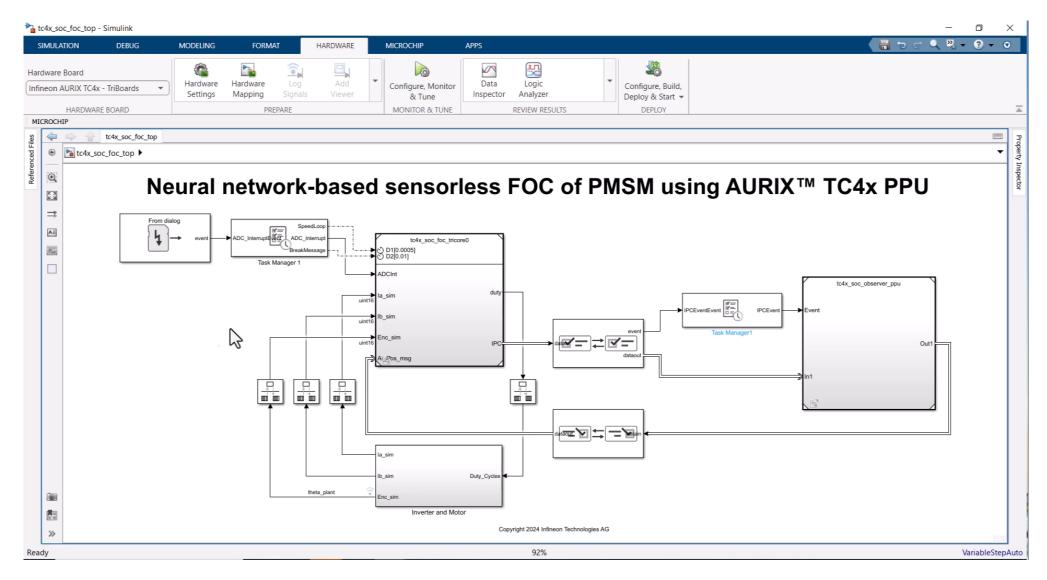






### NN-based sensorless FOC of PMSM using AURIX<sup>™</sup> TC4x PPU<sup>™</sup> Development process: Deployment





infineon

### NN-based sensorless FOC of PMSM using AURIX<sup>™</sup> TC4x PPU<sup>™</sup> Development process: Measure



Orefye         File       Options       Vew       Help         Infineon       Setup connection         Permanent Magnet Synchronous Motor (PMSM)         Setup connection         Notor Position Observer Using Artificial Intelligence (Al)         Implemented on Parallel Processing Unit (PPU)       Output											
Graph Scope Signal	Flow (1: Speed Ref.[rpm] Y: 1000 /D/v Value: 1100	2: Speed Meas. [rpm] Y: 1000 (Div Value: 1083.78	3: Speed Ext [rpm] Y: 1000 /D/v Value: 1100.53	4: Speed Enc [rpm] Y: 1000 /Div Yalue: 1083.78				Control Run/Ste	Reset zoom		
X: 2 s/Div Speed Target (rpm) 0 OneEye Version 2.52.0			U,	13.3186s St Enco	1				Control Info Hide		



#### Wrapping Up



AI-based virtual position sensor can increase the quality of the electric motor position sensing. The AI can take the position sensing of a "cheap" physical sensor and – due to training with high-quality data – transform it into a high-quality position sensing

AURIX<sup>™</sup> TC4x hardware support package enables code generation and software built for multi-core applications including PPU

Model driven development maximizes re-use of existing projects and decreases the engineering effort

With MathWorks as a partner, Infineon Technologies AG offers complete ecosystem for model driven development and closed-loop validation on different abstraction levels



#### MATLAB EXPO



© 2024 The MathWorks, Inc. MATLAB and Simulink are registered trademarks of The MathWorks, Inc. See *mathworks.com/trademarks* for a list of additional trademarks. Other product or brand names may be trademarks or registered trademarks of their respective holders.

